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Larry Baoqi Li

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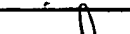

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
had		5,781,389	7/14/1998	Fukazako et al.	361	56	
had		5,969,929	10/19/1999	Kleveland et al.	361	111	
had		5,173,755	12/22/1992	Co et al.	257	361	
had		5,838,146	11/17/1998	Singer	323	270	
had		5,631,793	5/20/1997	Ker et al.	361	56	
had		5,936,284	8/10/1999	Ravanelli	257	361	

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	<p>Chatterjee, A Low Voltage Triggering SCR for ON-Chip ESD Protection at Output and Input Pads, IEEE Electron Device Letters, Vol. 12, No. 1, January 1991, pages 21-22</p>
	<p>Duvvury, Rountree, & Adams, Internal Chip ESD Phenomena Beyond the Protection Circuit, IEEE Transactions on Electron Devices, Vol. 35, No. 12, December 1988, pages 2133-2138</p>

EXAMINER

DATE CONSIDERED _____

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.